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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

Sheet 1

of 2

**Complete if Known**

Application Number	10/634,268
Filing Date	August 5, 2003
First Named Inventor	Jin-Man Han
Art Unit	
Examiner Name	
Attorney Docket Number	P2341-C-1

**U. S. PATENT DOCUMENTS**

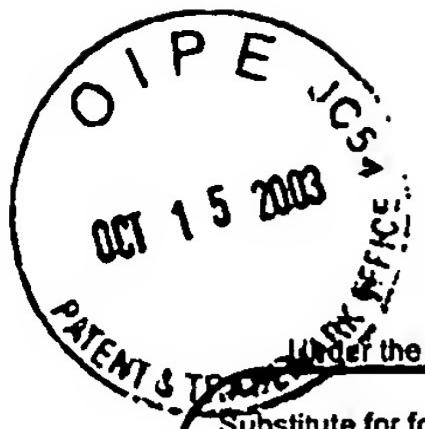
## **FOREIGN PATENT DOCUMENTS**

Examiner Signature		Date Considered	4/04
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PTO/SB/08B (08-03)

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Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

2 of 2

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### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DL		HIRANDO, H., et al., 2V/100-ns 1T/1C Nonvolatile Ferroelectric Memory Architecture with Bitline-Driven Read Scheme and Nonrelaxation Reference Cell, IEEE Journal of Solid-State Circuits, 1997, pages 649-54, Vol. 32, No. 5	

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